# Exercise 4: Laser writer + contact patterning

Location: CMI

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# 1. Summary

In this exercise you will perform the last set of device fabrication steps resulting in MoS<sub>2</sub> devices with electrical contacts. Just like in exercise 3, you will make use of photolithography, employing a laser writer to write a pattern in a photoresist layer. This time, you will use two layers of positive photoresist. This will be followed by metal evaporation for contacts and lift-off, the step in which the resist film is dissolved, leaving behind the electrical contacts on the patterned MoS<sub>2</sub> in predetermined locations. The contact deposition is carried out using a metal evaporator. Since this step requires several hours (this is how long it takes to achieve a vacuum level in the evaporator suitable for contact deposition), this step will be carried out by the assistant after the end of the exercise. Similarly, liftoff is usually carried out by soaking the wafer in a solvent during several hours, so this step will also be done outside of the scheduled course hours.

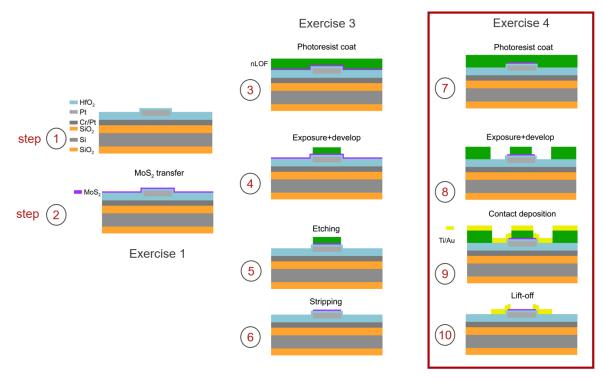


Figure 1. Overview of the complete process flow for the device fabrication in this course. The current exercise is outlined with the red rectangle.

This exercise is conceptually very similar to Exercise 3, both use the same types of equipment for resist coating and exposure. The difference is that here, you use the exposed pattern as a mask for metal deposition and that you will use a bilayer photoresist.

## 2. Background

A big portion of micro/nanofabrication procedures could be bluntly categorized into procedures for either removing (subtractive techniques) or adding materials (additive techniques). If you are removing materials, this is referred to as etching, if you are adding materials, this is called deposition.

Since we would like to deposit metals for electrical contacts only in predetermined locations, we will use a mask in the form of a patterned resist. In order to pattern the resist, one can use either light (laser, UV lamp) during photolithography or electrons for e-beam lithography. The interaction with light or the e-beam provokes a chemical change in the resist. Depending on the resist, one could either be breaking chains in a polymer or crosslinking the chains. After the exposure, we perform development to remove either the exposed or the unexposed regions of the resist. We distinguish two basic types of resists, depending on how they react to the beam and development. One type is called negative tone resist, meaning that the portions of the resist that are exposed to the laser are the ones that remain on your sample after the development step. In exercise 4, you use a positive tone resist. In the case of positive tone resists, the exposed areas are removed during development and the unexposed areas remain.

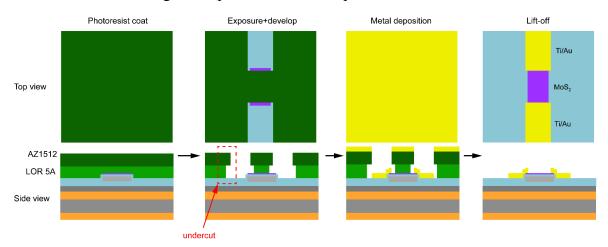


Figure 2. Overview of the process for patterned contact deposition.

The resist exposure and development step will be followed my metal deposition and lift-off, Figure 2. In order to facilitate lift-off, we will be using here the bilayer resist technique, meaning that instead of a single resist layer, we will be coating the sample with two different photoresists, first LOR 5A, followed by AZ1512. In some cases where bilayers are used, the one on the bottom requires a lower exposure dose than the one on top. When we expose this stack of resists at the dose which is optimal for the top resist, the one on the bottom will be slightly overexposed, meaning that the patterns in this layer will be a little bigger than those in the top layer, resulting in an undercut (Figure 2, second panel from the left). In our particular case, LOR is not photosensitive but it gets removed during the development step through the windows opened in the AZ1512 film, resulting in an undercut. Once the metal film is evaporated, because of the presence of the undercut, the metal film forming the contacts will not come into contact with the resist. This will result in smoother contact edges and much easier lift-off.

For the actual evaporation step, we will use an electron-beam evaporator, with the schematic shown in Figure 3. It is based on a vacuum chamber and is typically pumped down to the <10<sup>-5</sup> mbar or range of pressure. On the bottom of the chamber is a material source in the form of a water-cooled copper base with a conical perforation into which we place a crucible (made

of a high-melting point material such as graphite, BN, tungsten etc) containing the metal to be evaporated. The material is melted using a stream of electrons emitted from a heated filament and with a trajectory that is bent using a magnetic field. The metal is heated to its boiling point and starts to evaporate, resulting in a stream of metal atoms which condenses on the substrate. The stream of evaporating atoms can be interrupted using a moveable shutter. Substrates are placed upside-down, in order to minimize possible contamination from falling particles. The sample holder is rotated using a motor in order to assure uniformity of deposition. The thickness of the deposited metal is precisely measured using a quartz thickness monitor (QCM). It consists of a crystal of quartz, a piezoelectric material, oscillating under an application of an AC voltage. Shifts in its resonant frequency are related to the mass of the oscillator, allowing us to determine the thickness of the deposited film.

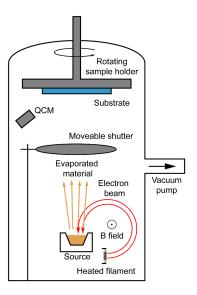
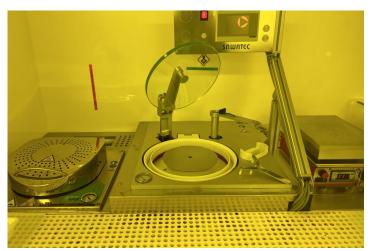


Figure 3. Schematic of an electron-beam evaporator.

For the case of the evaporator used for this exercise, the entire deposition process is computer controlled and requires minimal input from an operator.

# 2.1. Description of the equipment used

### Sawatec SM-200 for photoresists



Sawatec SM-200 is a manual coater photosensitive resists for either wafers or chips. There are several different vacuum chucks available depending on the size of your wafer or chips. Notice

that it is important to use a chuck which is smaller than you sample, otherwise excess resist will clock the vacuum. Right next to the coater you will find two hotplate which are used for the softbake of your resists.

### Arias Base Z13

This wet bench will be used for development of the resist after the exposure step. The developer



solution is a base chemistry and will be poured in a beaker where the chips will be submerged for a certain development time. Another beaker with DI water will be used for removing the developer from the chip and consequently stop the development.

#### **MLA150**



MLA150 is mask-less aligner, a laser writer that makes it possible to write a pattern directly into a photoresist layer, without having to produce a photomask first. This is done by scanning the sample directly under the laser beam. The resolution is limited by the photoresist (length of the molecular chains in the polymer used), light wavelength and numerical aperture of the optics, resulting in a typical resolution of around 1  $\mu$ m.

#### LAB600 evaporator





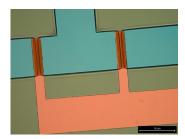


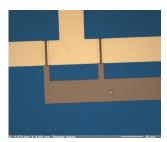
Figure 4. Left: Lab600 evaporator. Middle: e-beam evaporation source. The material is placed in the circular crucible in the middle and heated using an electron beam. Right: boat for evaporation via Joule heating.

The evaporator model Lab600 made by company Leybold Optics is piece of equipment allowing us to deposit metallic or insulating thin films on substrates placed about a heated material source. The material heating is achieved either by bombarding it using an electron beam or by placing it in metallic boat which is then heated via an electrical current (Joule heating). Evaporation is carried out in high vacuum (on the order of 10<sup>-5</sup> mbar or better), in order to minimize the collisions between gas molecules and the stream of evaporating materials as well as possible chemical reactions (mostly oxidation). Multiple materials can be loaded in the same time, allowing the deposition of different films in a single evaporation run which is controlled by a computer and carried out automatically.

The special feature of the Lab600 is a very large distance between the source and the deposition substrate, on the order of 1 m. This results in a near perpendicular angle of deposition and high uniformity of the deposition over the surface of the wafer, making it especially suitable for deposition of metal films for lift-off.

For this device fabrication step, we deposit a contact stack consisting of a 2 nm thick Ti film acting as an adhesion layer and first contact layer to MoS<sub>2</sub>, followed by a 100 nm think Au film. We know from experience that such films reliably result in good electrical contacts to MoS<sub>2</sub>.





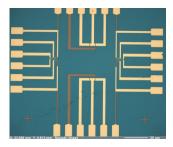


Figure 5. Optical micrographs of the device during different phases of fabrication. Left: after exposure and development. Center and right: after evaporation and lift-off.

## 3. Description of experiments and tasks

Following is the overview of the tasks and operations to be carried out in this exercise. The main goal is to pattern a photoresist layer and use it for subsequent deposition of electrical contacts on predetermined locations on the MoS<sub>2</sub> film.

# 3.1. Photoresist coating (Location: Zone 13 – Sawatec - Coating)

#### **Coating LOR**

- 1. Blow dry the chip with a N2 gun for removing any potential dust or particles.
- 2. Place your chip on a hotplate at T=100°C for a few minutes for dehydration.
- 3. Cool down for 1min
- 4. Install the small vacuum chuck for chips on the coater.
- 5. Place your chip on the spincoater and start the vacuum.
- 6. With a pipette, drop a few drops of LOR resist on the chip and start the coater with the recipe CHIP 6000RPM
- 7. Put the chip on the hotplate at 190°C for 4min10sec.
- 8. Cool down for 1min

### **Coating AZ1512**

- 1. Place your chip on the spincoater and start the vacuum.
- 2. Coat AZ1512 with recipe CHIP 6000RPM
- 3. Put the chip on the hotplate at 100°C for 1min30sec.
- 4. Cool down for 1min
- 5. Store the chip in a light-sensitive sample holder (black box).

### 3.2. Photoresist exposure (Location: Zone 15, MLA150)

1. Load chip now, first we align with the markers on the chip (the crosses).

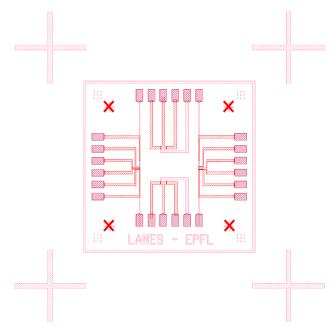


Figure 6. Alignment positions – Manual alignment in the crosses

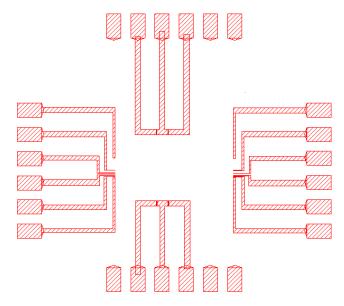


Figure 7. The pattern to be exposed – contacts

## 3.3. Development (Location: Zone 13, Arias Base Z13)

- 1. Find two beakers, one for base (developer) and one for rinse (DI water).
- 2. Submerge the chip with a tweezer into the developer and shake it gently for 1min10sec.
- 3. Transfer the chip to the DI water beaker and shake for 1min additionally.
- 4. Store the chip in a light-sensitive sample holder (black box).

# 3.4. Sample inspection

Verify that the sample has been correctly exposed and developed using one of the optical microscopes in CMI.

### 3.5. Evaporation (Location: Zone 4, Lab600)

- 1. Load chip taped to wafer in LAB600
- 2. Select recipe 201: Ti/Au with 2nm Ti and 80nm Au.

### 3.6. Final inspection and resist stripping (LANES Lab; done by the assistant)

- 1. Record the optical image of the chip using the microscope (Olympus BX51M).
- 2. Put the chip in a DMSO bath to lift-off the metal from chip.

# 4. Summary of experiments and tasks

- 1. Chip coating with the photoresist
- 2. Photoresist exposure and development
- 3. Contact deposition

# 5. Questions for the report

In the report, please show on the following:

1. Optical images of the sample after exposure and development